

669207-00E2460

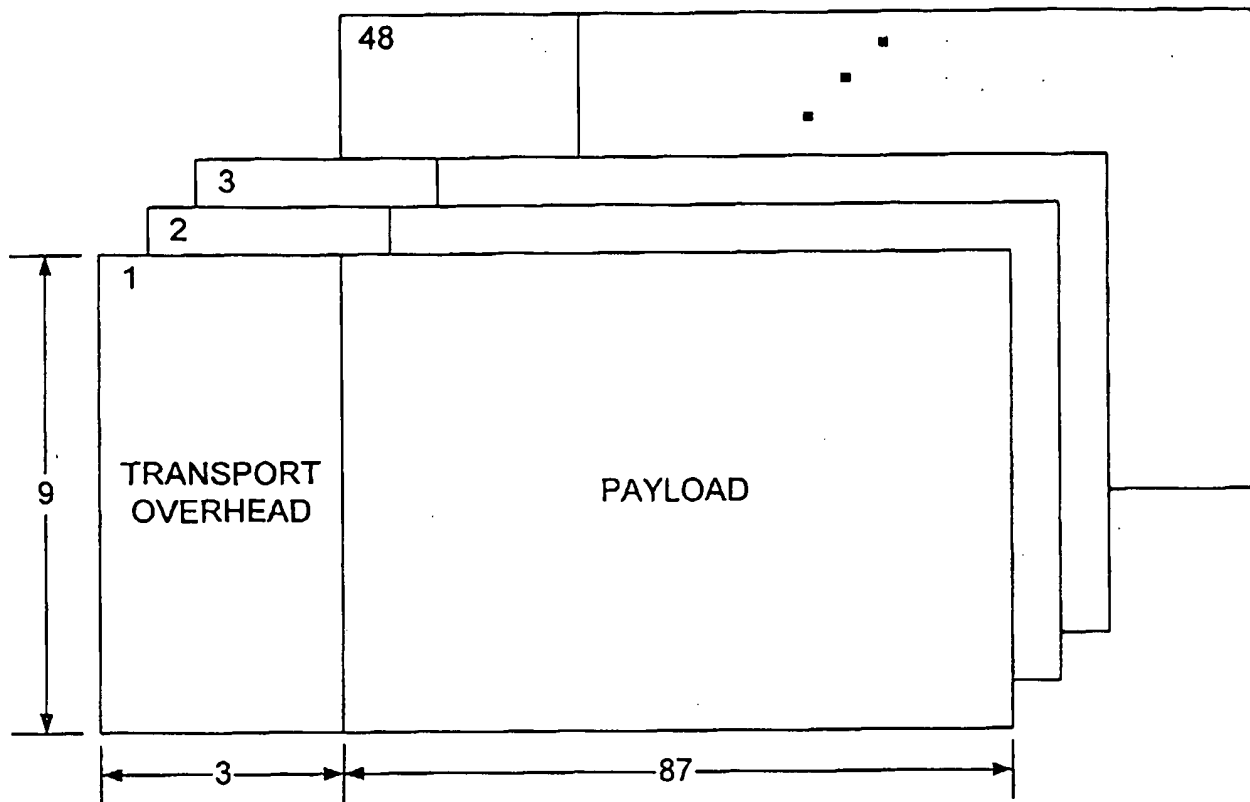


Fig. 1
Prior Art

66920T" 00E/2460

OC-3c #1	1	17	33
OC-3c #2	2	18	34
OC-3c #3	3	19	35
.			
.			
.			
OC-3c #16	16	32	48

Fig. 2
Prior Art

OC-12c #1	1	2	3	4	17	18	19	20	33	34	35	36
OC-12c #2	5	6	7	8	21	22	23	24	37	38	39	40
OC-12c #3	9	10	11	12	25	26	27	28	41	42	43	44
OC-12c #4	13	14	15	16	29	30	31	32	45	46	47	48

Fig. 3
Prior Art

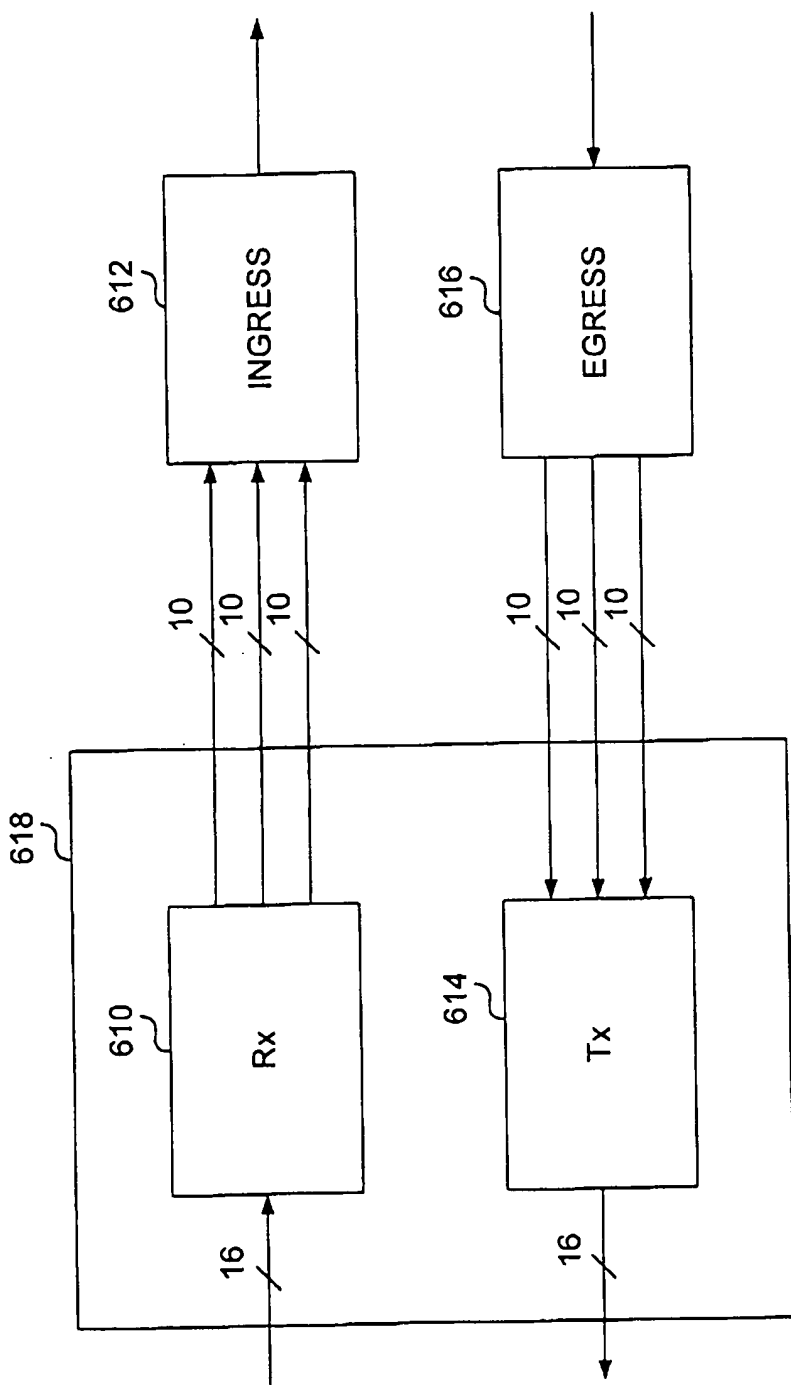


Fig. 5

FROM SWITCH
CONTROLLER 510

CONCATENATED
TIME SLOT
ID MEMORY

*610

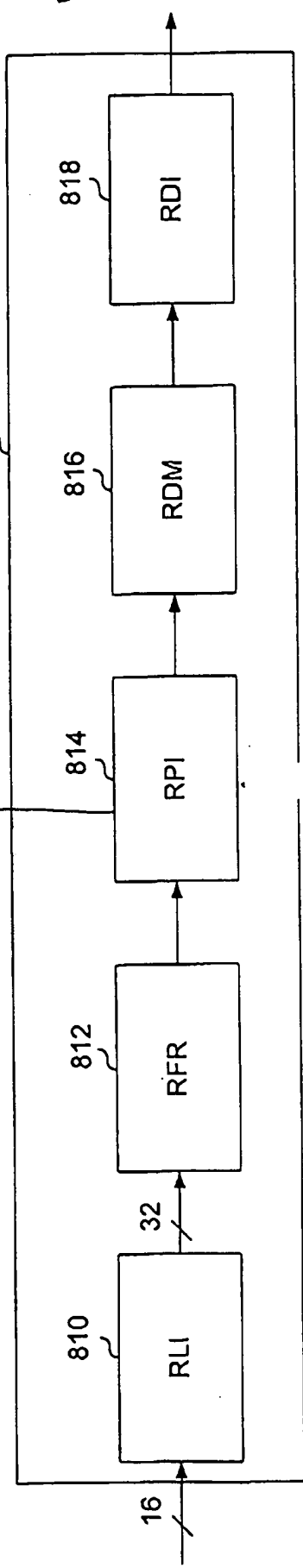


Fig. 6

09427300-102693

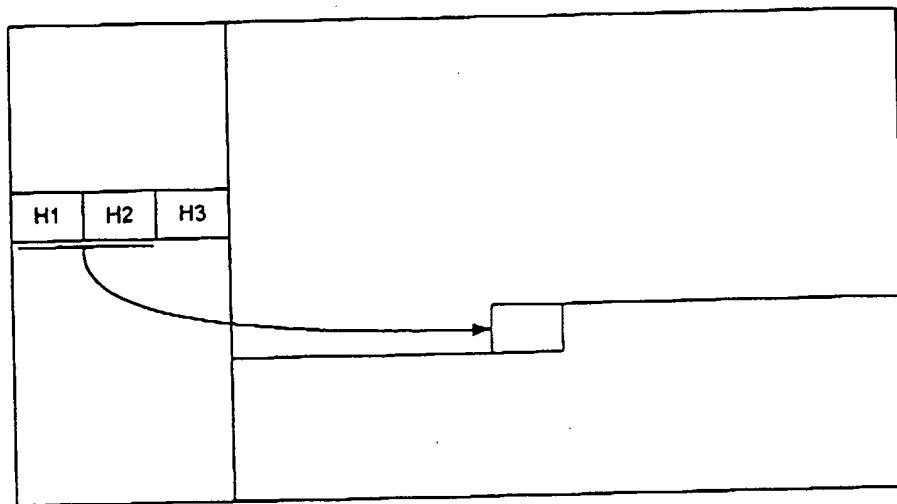


Fig. 7

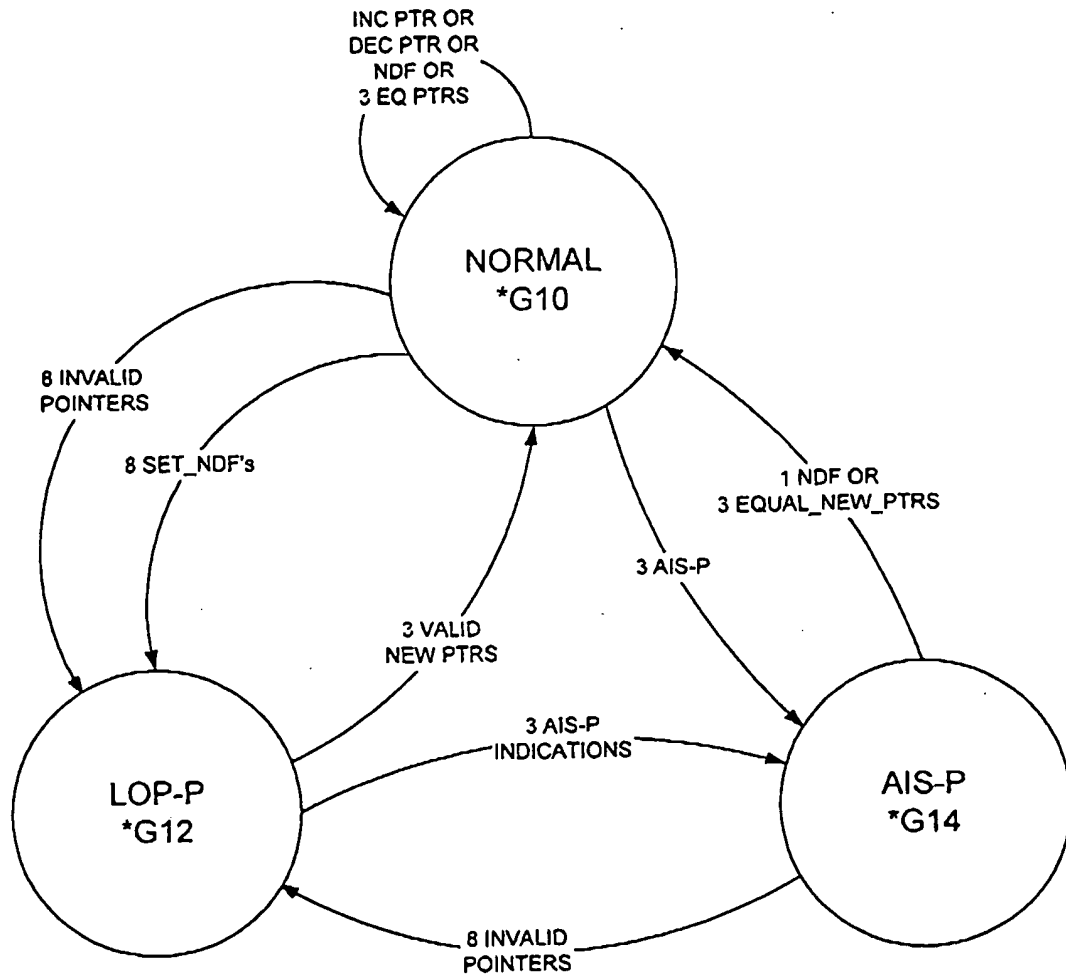


Fig. 8

STS-3C

STS-1 #1

POINTER

STS-1 #2

CONCAT IND

STS-1 #3

CONCAT IND

Fig. 9(a)

PARENT/CHILD

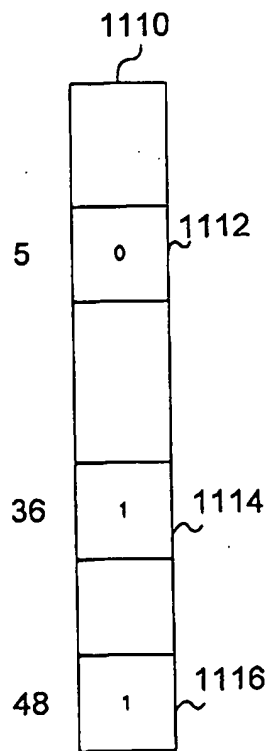


Fig. 9(b)

PARENT ID

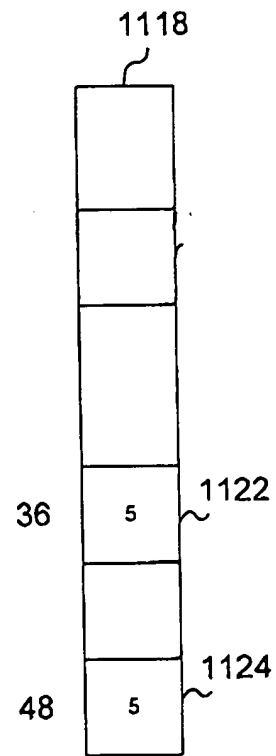


Fig. 9(c)

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FROM
CONTROLLER 510

614

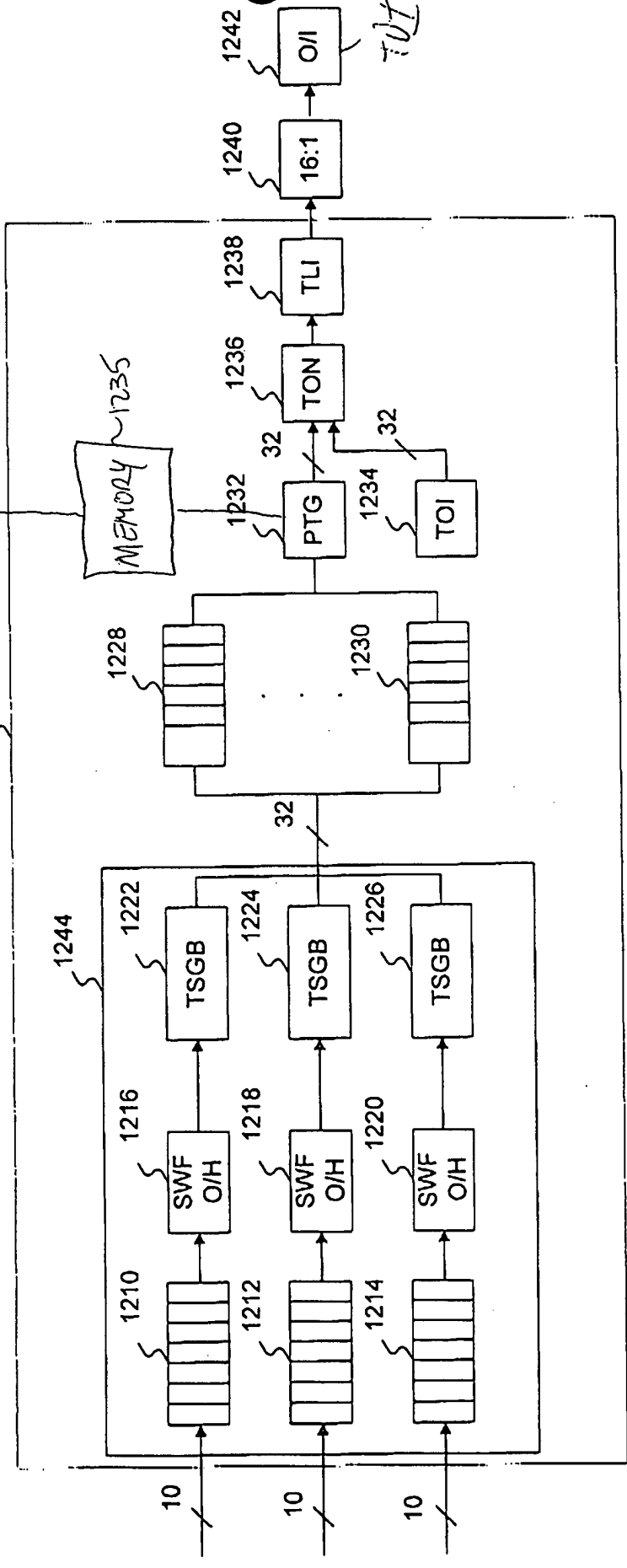


Fig. 10

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		CAT_IND	STS_ID	1310
PARENT	0	0		1312
CHILD 1	4	1	0	
CHILD 2	6	1	0	
	47			

Fig. 11

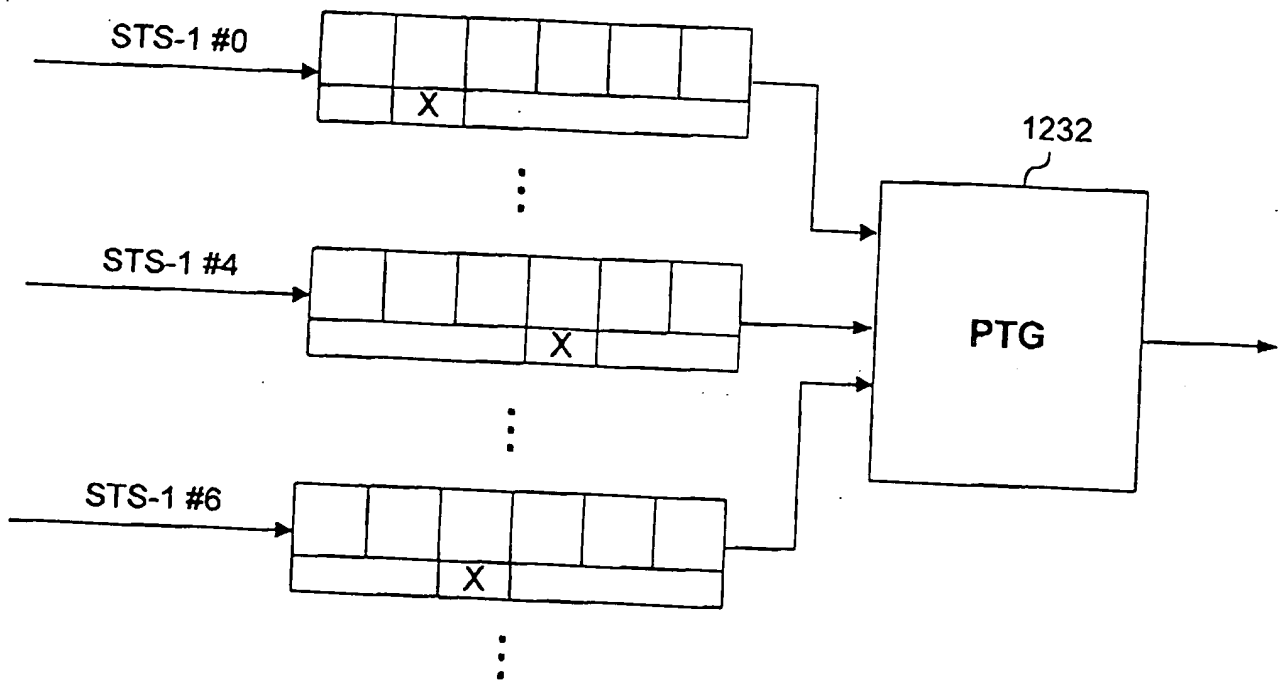


Fig. 12